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ON MANUFACTURING TECHNOLOGY 2024

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(Haeundae Beach) | Busan, Korea



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Jae-Dong Lee received a BS in Chemical Engineering from Yonsei University and an MS and Ph.D. in Chemical Engineering from Korea Advanced Institute of Science and Technology (KAIST). In 1998, he joined the Semiconductor R&D Center of Samsung Electronics as a CMP engineer. He participated in various R&D activities in the areas of CMP technology such as slurry, pad, metrology including equipment H/W and advanced process control (APC). He was dispatched Belgium as a CMP assignee of IMEC's advanced interconnect program for 2 years from 2005 to 2006. In 2014, he moved Cabot Microelectronics Co. (CMC) and worked for R&D and technical marketing at CMC's US headquarter (Illinois, USA) for 7 years. He engaged over 50 international patents and publications in CMP/Clean and semiconductor technology. Since 2022, he has served KC Tech Material Division as a GM of R&D and Sales.