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Kangchun Lee, Ph.D., has been an Assistant Professor in the Department of Electronic Engineering at Kyonggi University in Suwon, Republic of Korea, since 2023. His research focuses on Advanced CMP/Post-Cleaning Processes and cutting-edge materials based on Colloid- and Electrochemistry. Prior to joining Kyonggi University, he was a Staff Engineer at the Semiconductor R&D Center, Samsung Electronics from 2020 to 2023. During his three years at Samsung Electronics, he participated in research on the GAAFET (Gate-All-Around FET) based sub-3nm foundry CMP process and integration, from development to mass production transfer.

He received a Ph.D. degree in Energy Engineering and a B.S. degree in Materials Science and Engineering from Hanyang University in Seoul, Republic of Korea, respectively. Currently, he serves as a Program Committee in International Conference on Planarization/CMP Technology (ICPT), a Topical Advisory Panel Member in Applied Sciences, and an Early Career Scholars Member in material science for the Journal of Materials Research.