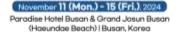


KOREAN INTERNATIONAL SEMICONDUCTOR CONFERENCE & EXHIBITION ON MANUFACTURING TECHNOLOGY 2024

KISM 2024 BUSAN







(Samsung Electronics, Korea)



Woo Jin Jung, Ph.D. has been DRAM process development engineer at SAMSUNG semiconductor R&D center since 2009 with capacitor dielectric materials and lithography patterning development. He recived BS/MS/Ph.D. of Physics with development of STM(Scanning Tunneling Microscopy) tool and Si surface analysis in Yonsei University, Seoul, Korea. Currently, He keeps working on DRAM lithography patterning development focusing on EUVL strategy from cost effective pespective and for next generation product.