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Dr. Deoksin Kil received his B.S. from POSTECH and earned his M.S. & Ph.D. from Seoul National University(SNU) in Material Science and Engineering(1999). He has been working at SK hynix as a senoir research fellow leading material development group in Infra Tech Center. He has been a member of technical program committee for VLSI since 2020. His current major research filed is to deveop all the base materials which are used for semiconductor processes such as photo resist, functional chemicals, CMP slurries, precursors and etch gases as well. He also have lots of experiences for capacitor dielectric masterials and tools for DRAM.