KOREAN INTERNATIONAL SEMICONDUCTOR CONFERENCE & EXHIBITION
ON MANUFACTURING TECHNOLOGY 2024

KISM 2024 BUSAN

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Paradise Hotel Busan & Grand Josun Busan
(Haeundae Beach) | Busan, Korea

Dr.   Jeongdong Choe

Senior Technical Fellow & SVP, TechInsights, Canada

Dr. Jeongdong Choe is a Senior Technical Fellow and SVP at TechInsights. He has over 32 years of hands-on experience in the semiconductor industry, R&D and reverse engineering analysis on DRAM, NAND/NOR FLASH, SRAM/Logic, and Emerging/embedded Memory devices such as MRAM, PCRAM, XPoint, ReRAM, and FeRAM. He worked for SK hynix and Samsung Electronics for over 20 years.

He has manufacturing expertise in high technologies including direct experience in the semiconductor process flow, process integration, unit process and tools for photo-mask, photo-lithography, plasma/wet etching, CMP, deposition, implantation, diffusion and annealing. He joined TechInsights and has been leading Memory-related projects, focusing on technology, device, and architecture design analysis. He regularly publishes semiconductor-related articles, blogs, technology trends, markets, and roadmaps. He also delivers annual memory seminars, memory technology updates, briefings, keynotes, and talks at TechInsights on-/off-line seminars, webinars, and global conferences.

■   Education

- Bachelor of Metallurgical Engineering, *Yonsei University, Korea*

- Master's Degree in Metallurgical Engineering, *Yonsei University, Korea*

- Ph. D of Electronics and Semiconductor Engineering, *Sungkyunkwan University, Korea*

■   Professional Experience

- GoldStar Electronics (LG Semicon, and then Hynix Semiconductor), R&D (1994-)

- Samsung Electronics, Semiconductor Business, R&D (1999-)

- TechInsights (2011-)

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